

AD9135/AD9136 QUALIFICATION PLAN			
TEST	CONDITIONS	SAMPLE SIZE	RESULTS
Latch-Up	JEDEC <i>JESD78</i>	3	Pass
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JESD22-C101</i>	3/voltage	+/-750V¹ +/-500V²
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001-2011</i>	3/voltage	+/-2500V¹ +/-750V²
High Temperature Operating Life (HTOL)	JEDEC <i>JESD22-A108</i>	2x35	May 2015

¹ RF pins excluded

² RF pins included

AD9135/AD9136 SPECIFICATIONS CHANGES

FROM: REV 0 DATA SHEET

JESD204B SERIAL INTERFACE SPEED SPECIFICATIONS

AVDD33 = 3.3 V, SIOVDD33 = 3.3 V, IOVDD = 1.8 V, DVDD12 = 1.2 V, CVDD12 = 1.2 V, PVDD12 = 1.2 V, SVDD12 = 1.2 V, $V_{TT} = 1.2$ V, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $I_{OUTFS} = 20$ mA, unless otherwise noted.

Table 1.

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit
HALF RATE	SVDD12 = 1.2 V \pm 5%	5.65		8.92	Gbps
	SVDD12 = 1.2 V \pm 2%	5.65		9.42	Gbps
	SVDD12 = 1.3 V \pm 2%	5.65		10.64	Gbps
FULL RATE	SVDD12 = 1.2 V \pm 5%	2.83		4.63	Gbps
	SVDD12 = 1.2 V \pm 2%	2.83		4.93	Gbps
	SVDD12 = 1.3 V \pm 2%	2.83		5.52	Gbps
OVERSAMPLING	SVDD12 = 1.2 V \pm 5%	1.42		2.31	Gbps
	SVDD12 = 1.2 V \pm 2%	1.42		2.46	Gbps
	SVDD12 = 1.3 V \pm 2%	1.42		2.76	Gbps

TO: REV A DATA SHEET

JESD204B SERIAL INTERFACE SPEED SPECIFICATIONS

AVDD33 = 3.3 V, SIOVDD33 = 3.3 V, IOVDD = 1.8 V, DVDD12 = 1.2 V, CVDD12 = 1.2 V, PVDD12 = 1.2 V, SVDD12 = 1.2 V, $V_{TT} = 1.2$ V, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $I_{OUTFS} = 20$ mA, unless otherwise noted.

Table 2.

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit
HALF RATE	SVDD12 = 1.2 V \pm 5%	5.75		8.92	Gbps
	SVDD12 = 1.2 V \pm 2%	5.75		9.42	Gbps
	SVDD12 = 1.3 V \pm 2%	5.75		10.64	Gbps
FULL RATE	SVDD12 = 1.2 V \pm 5%	2.88		4.63	Gbps
	SVDD12 = 1.2 V \pm 2%	2.88		4.93	Gbps
	SVDD12 = 1.3 V \pm 2%	2.88		5.52	Gbps
OVERSAMPLING	SVDD12 = 1.2 V \pm 5%	1.44		2.31	Gbps
	SVDD12 = 1.2 V \pm 2%	1.44		2.46	Gbps
	SVDD12 = 1.3 V \pm 2%	1.44		2.76	Gbps